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In re Patent Application of:
PIO

Serial No. **Not Yet Assigned**

Filing Date: **Herewith**

For: **ELECTRONIC MEMORY CIRCUIT
AND RELATED MANUFACTURING
METHOD**

) DATE OF DEPOSIT Feb. 9, 2001
)
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SUBMISSION OF PROPOSED MODIFICATIONS TO DRAWINGS

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Submitted herewith is a request for a proposed
drawing modification to label FIGS. 1-5 prior art and to add a
reference numeral to FIG. 6 as indicated in red ink.

Respectfully submitted,


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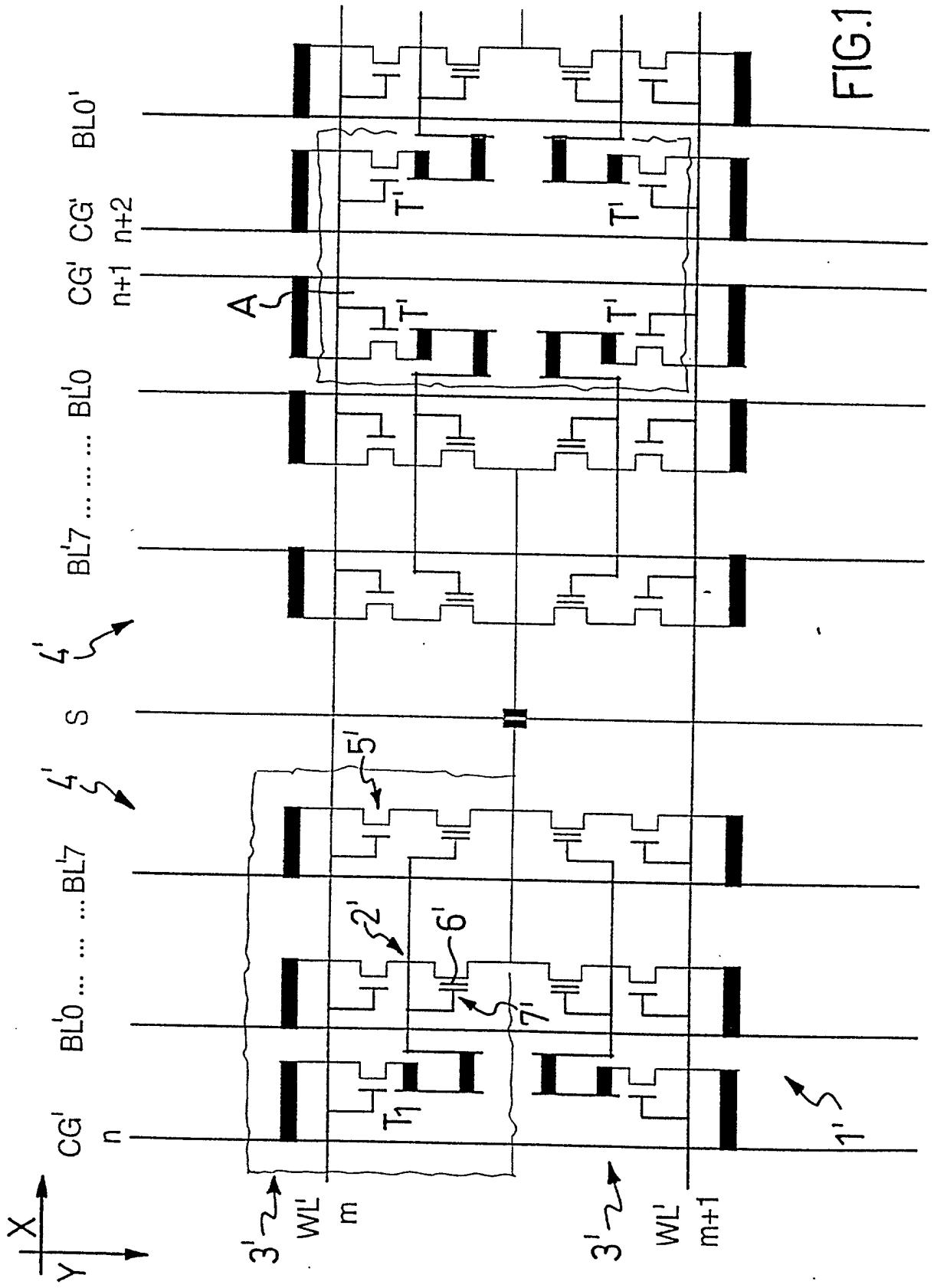


FIG.1

PRIOR ART

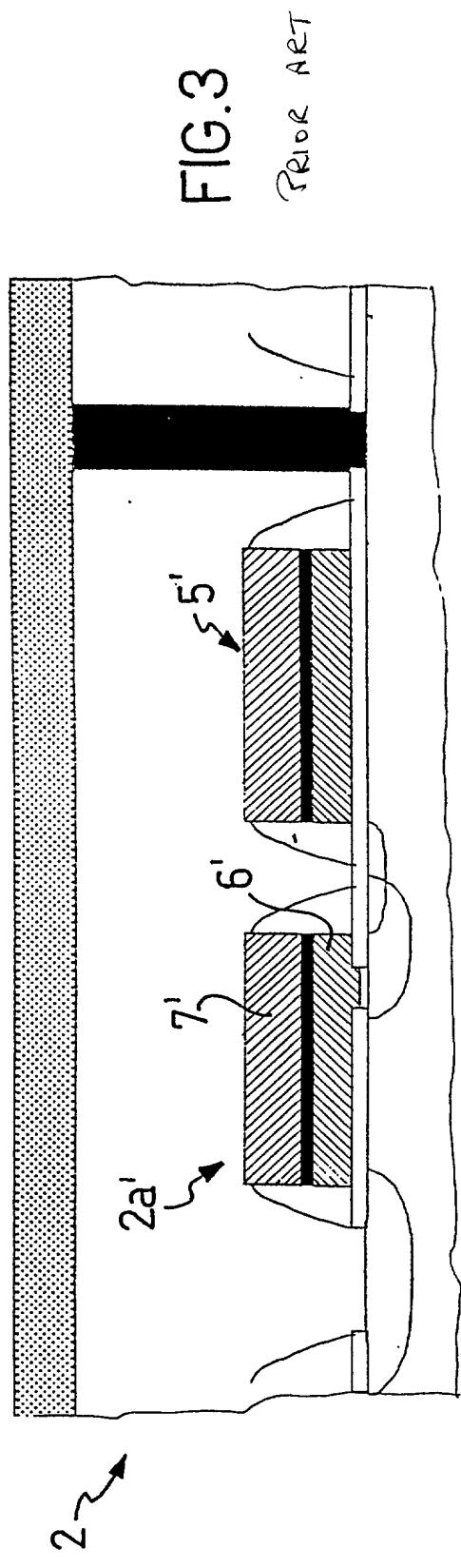
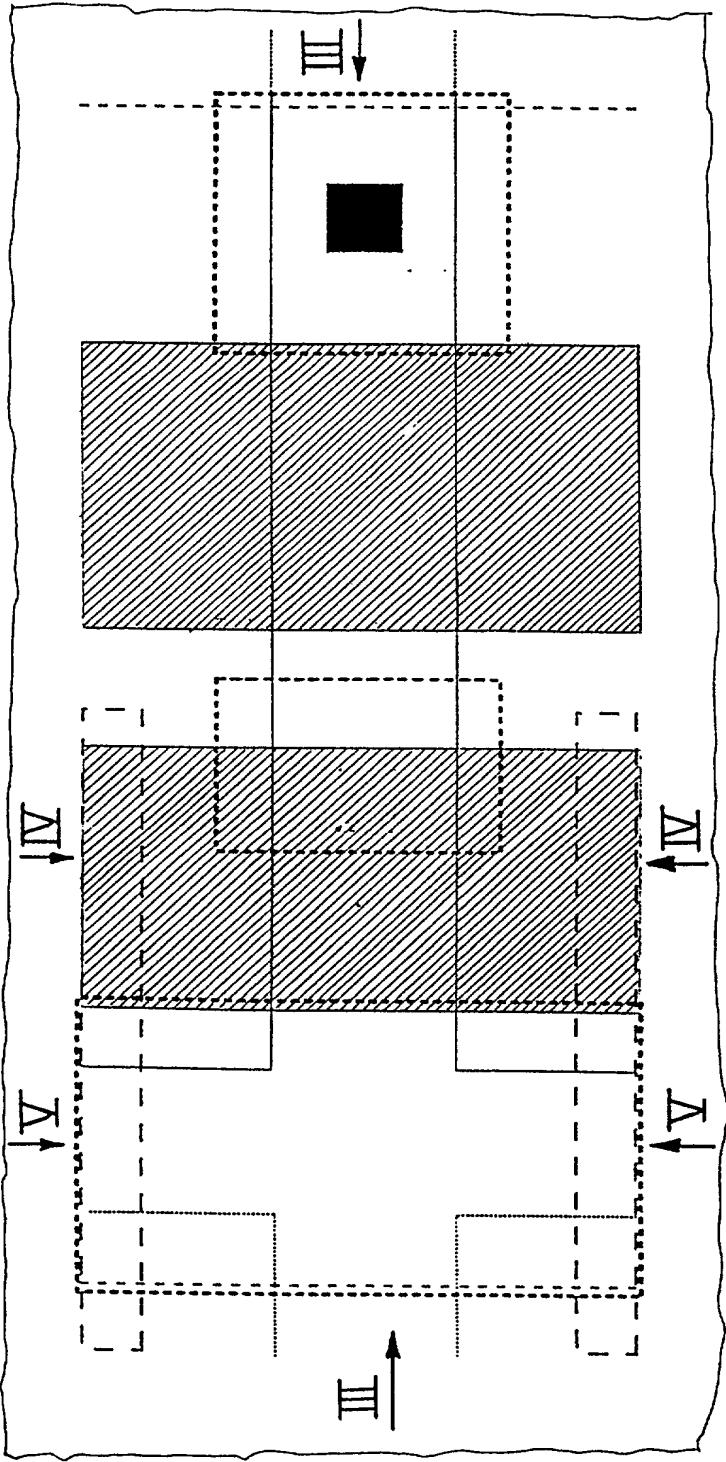


Figure 4 shows a cross-section of a composite structure. It consists of a base layer 10, a middle layer 20, and a top layer 30. The top layer 30 has a central opening 32 and a side slot 34. The side slot 34 is positioned such that it aligns with the central opening 32 when the structure is assembled.

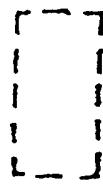


FIG.4

PRIOR ART

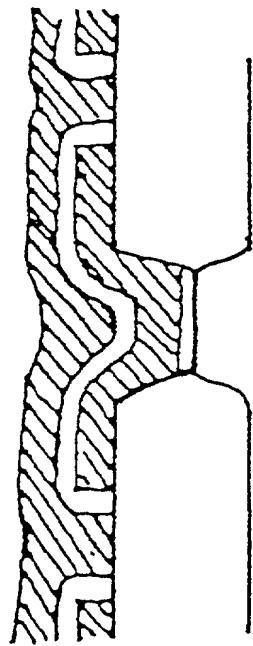


FIG.5

PRIOR ART

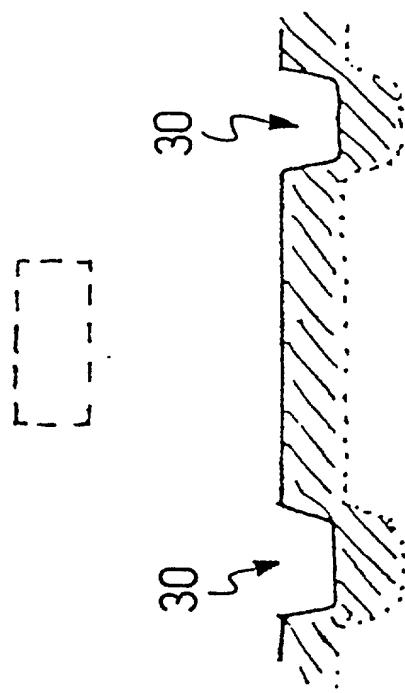
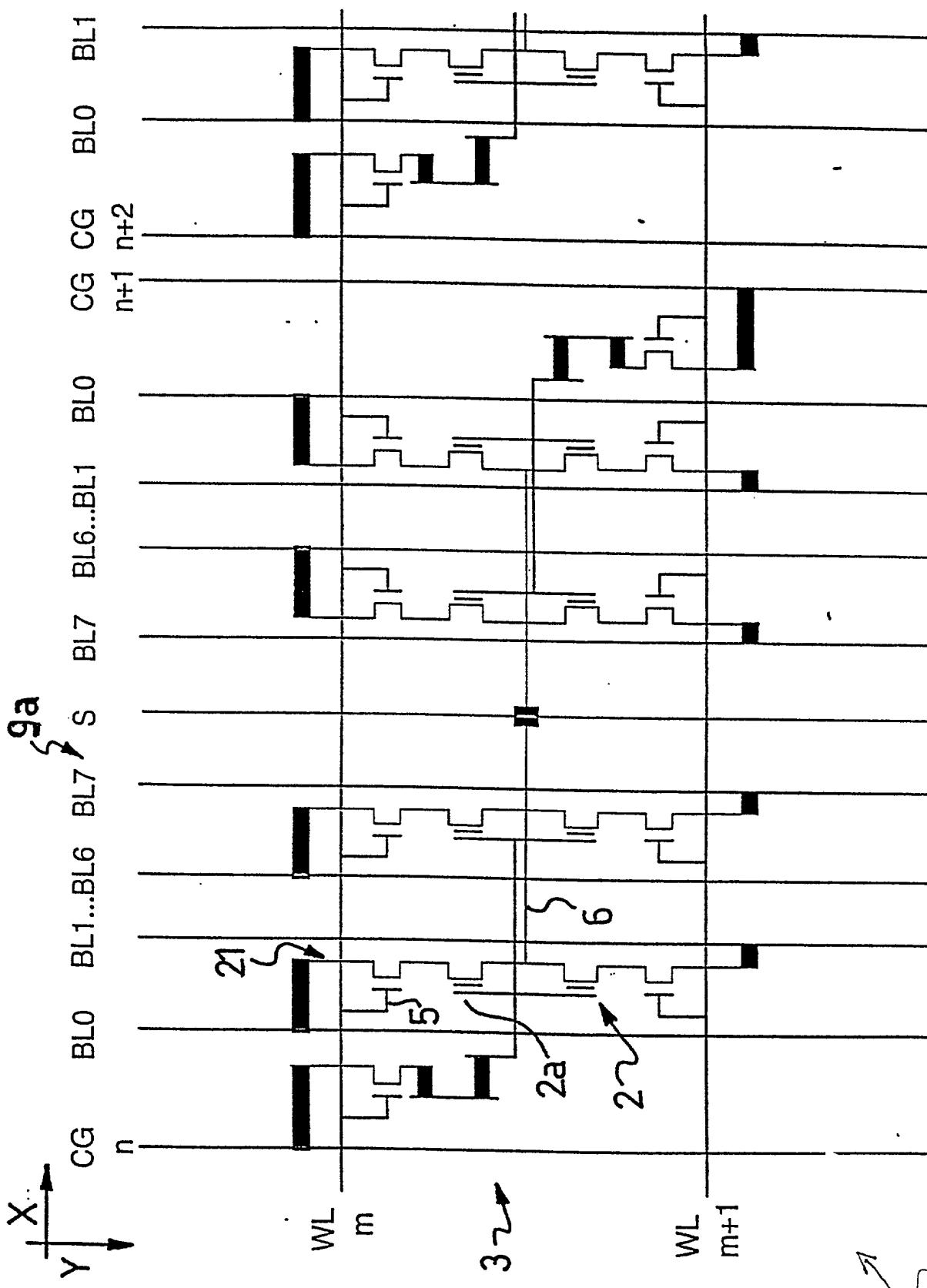


FIG.6



1

FIG.7

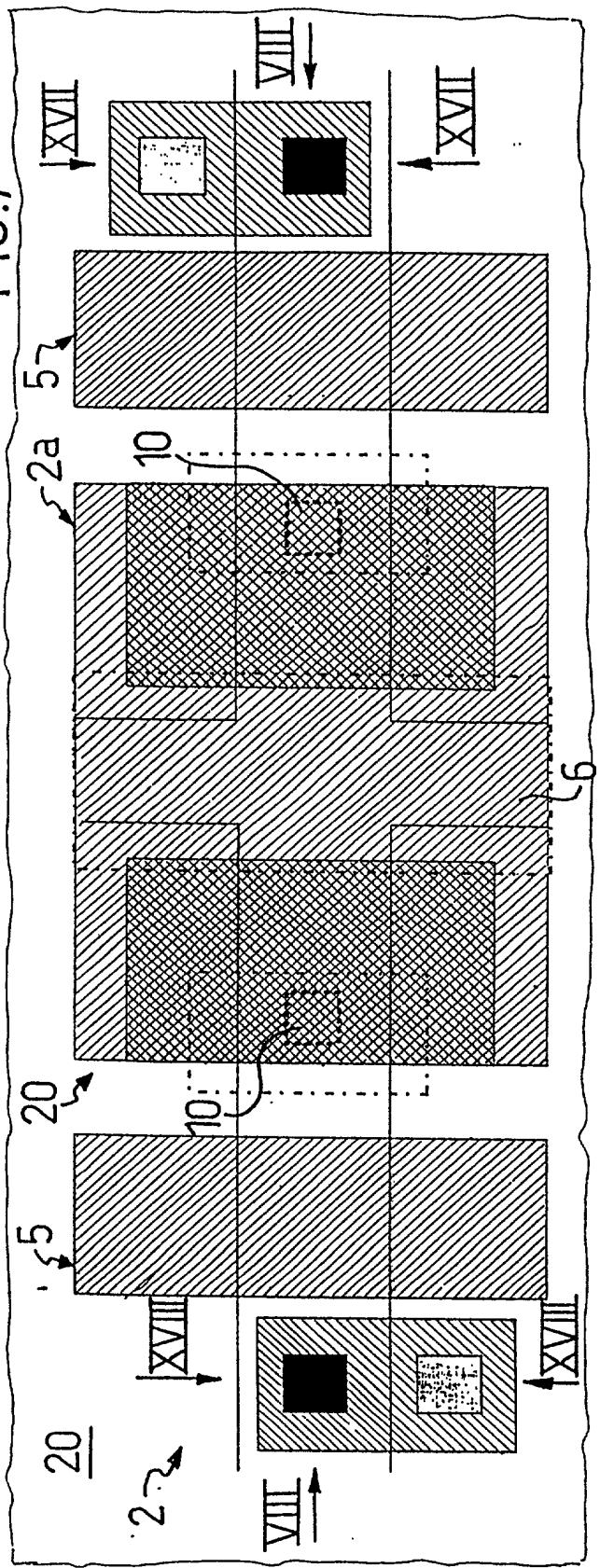
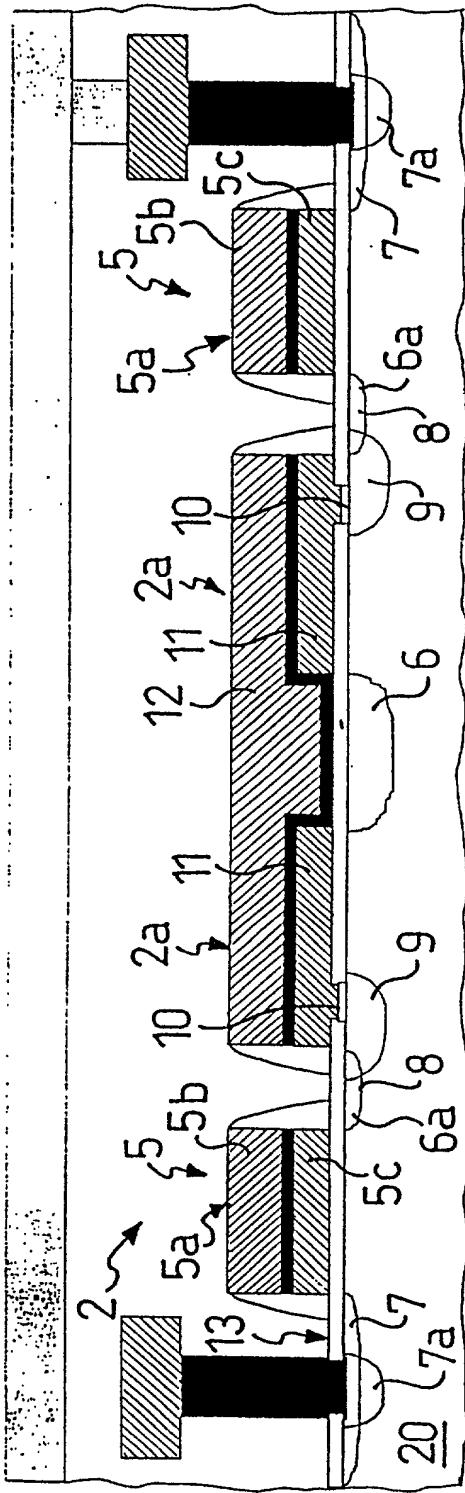


FIG.8



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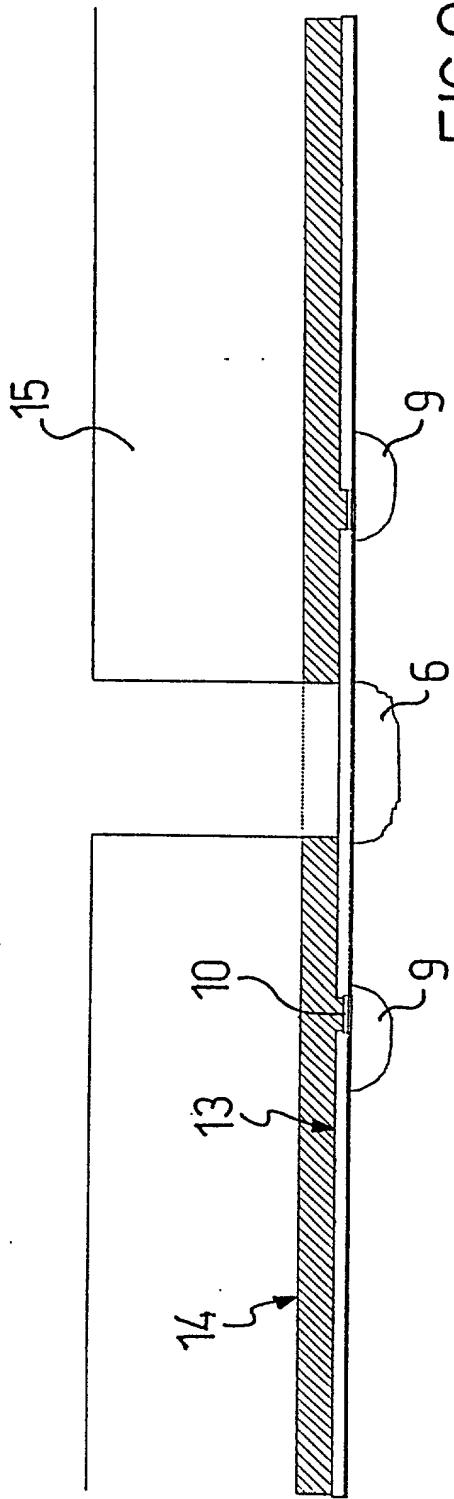


FIG.9

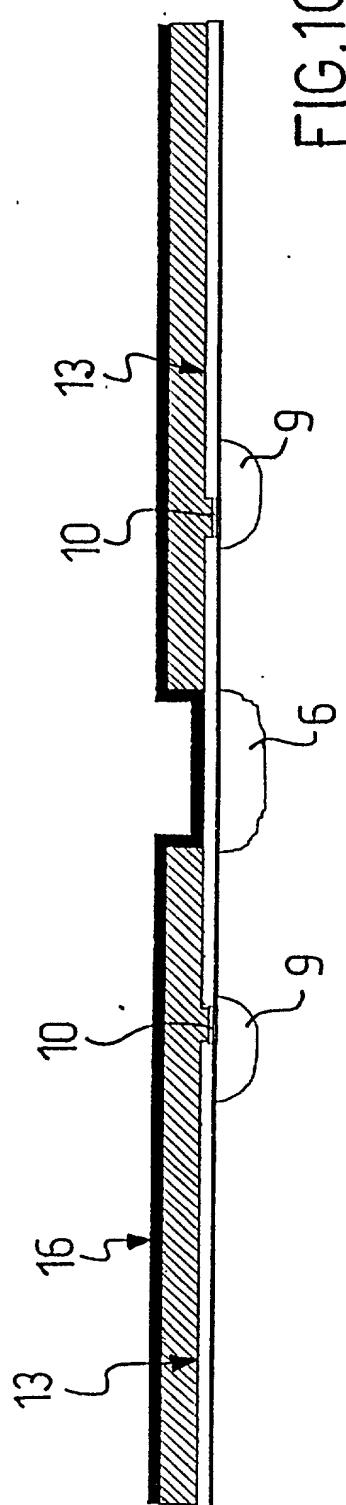


FIG.10

14 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100

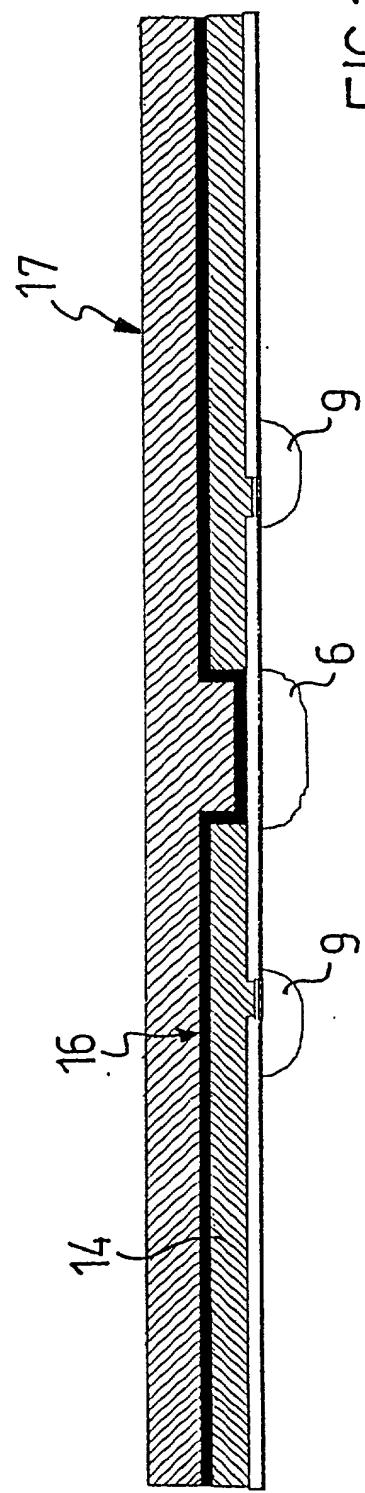


FIG. 11

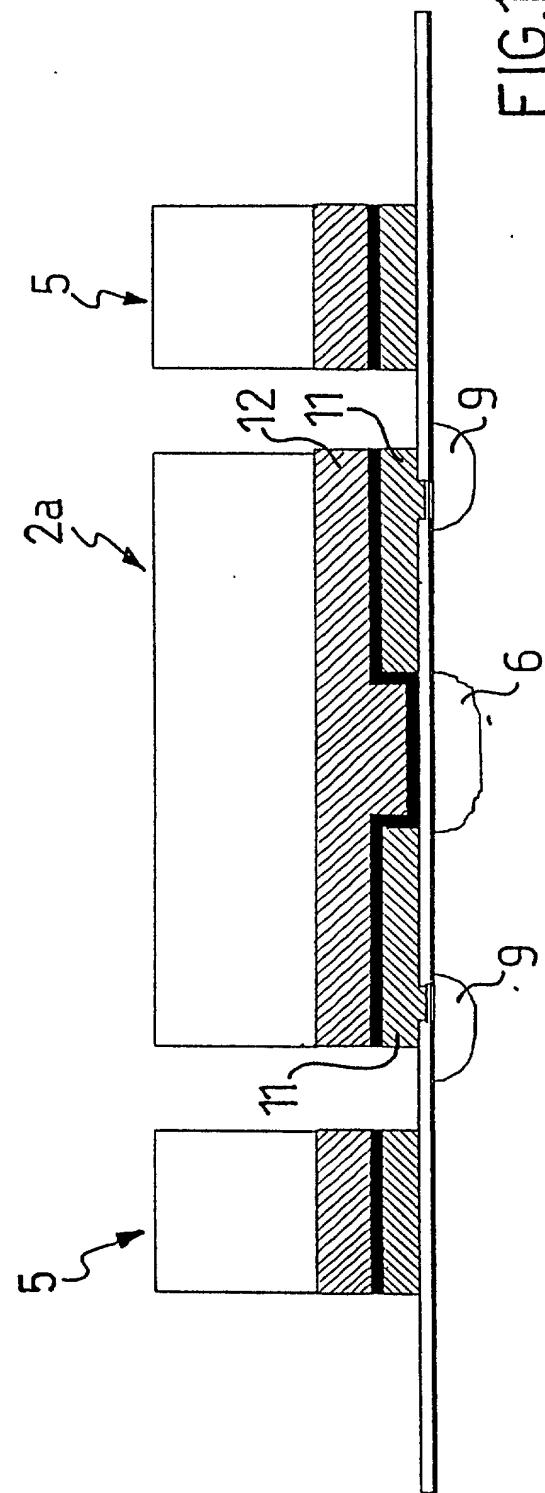


FIG. 12

18 18 18 18 18 18

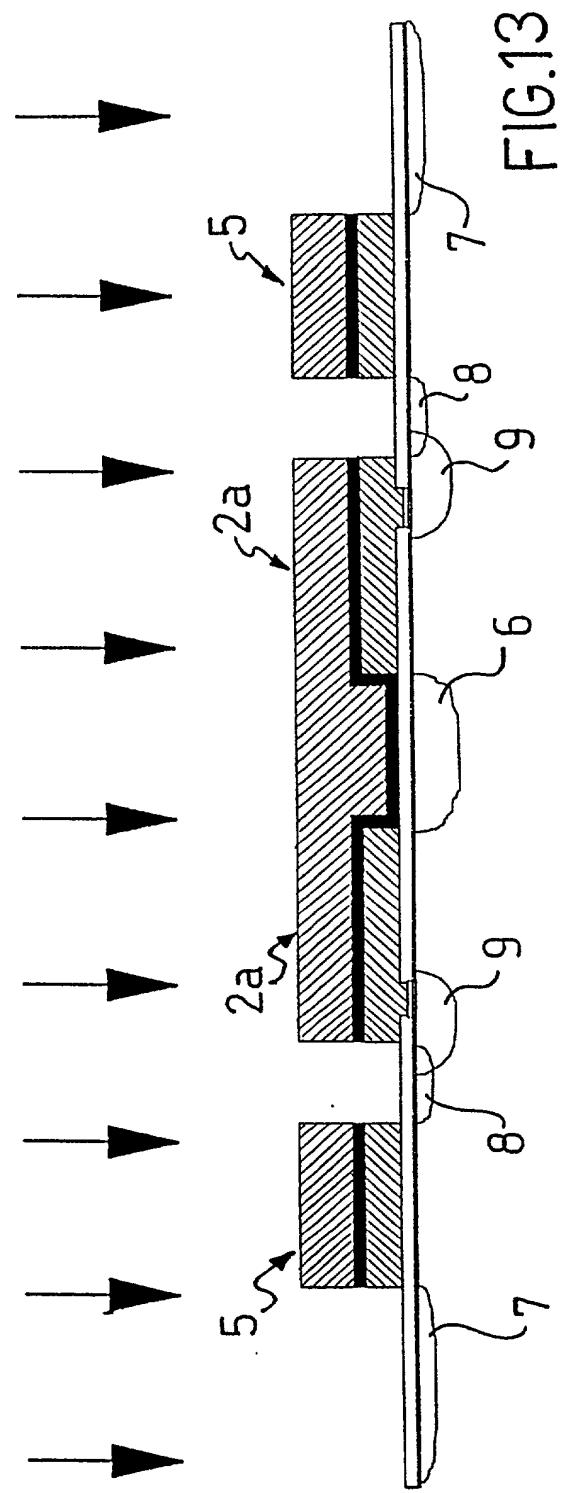


FIG.13

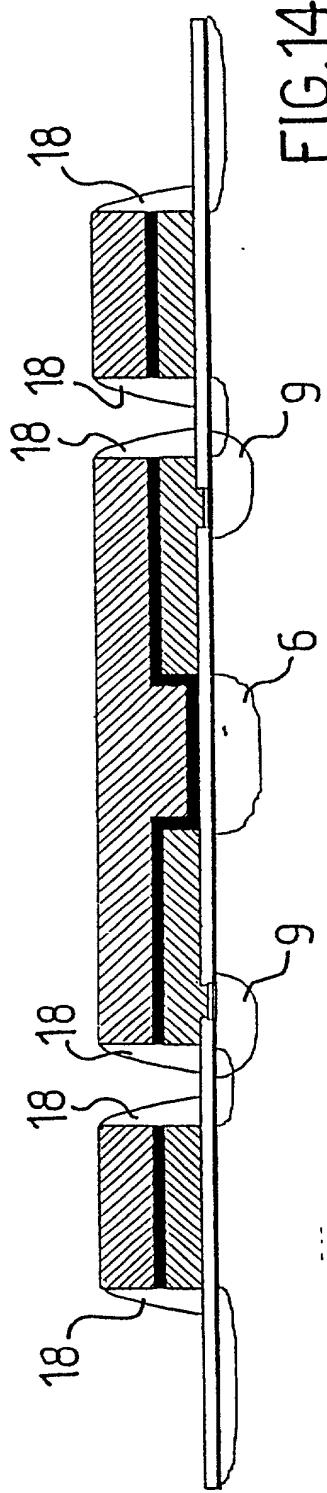


FIG.14

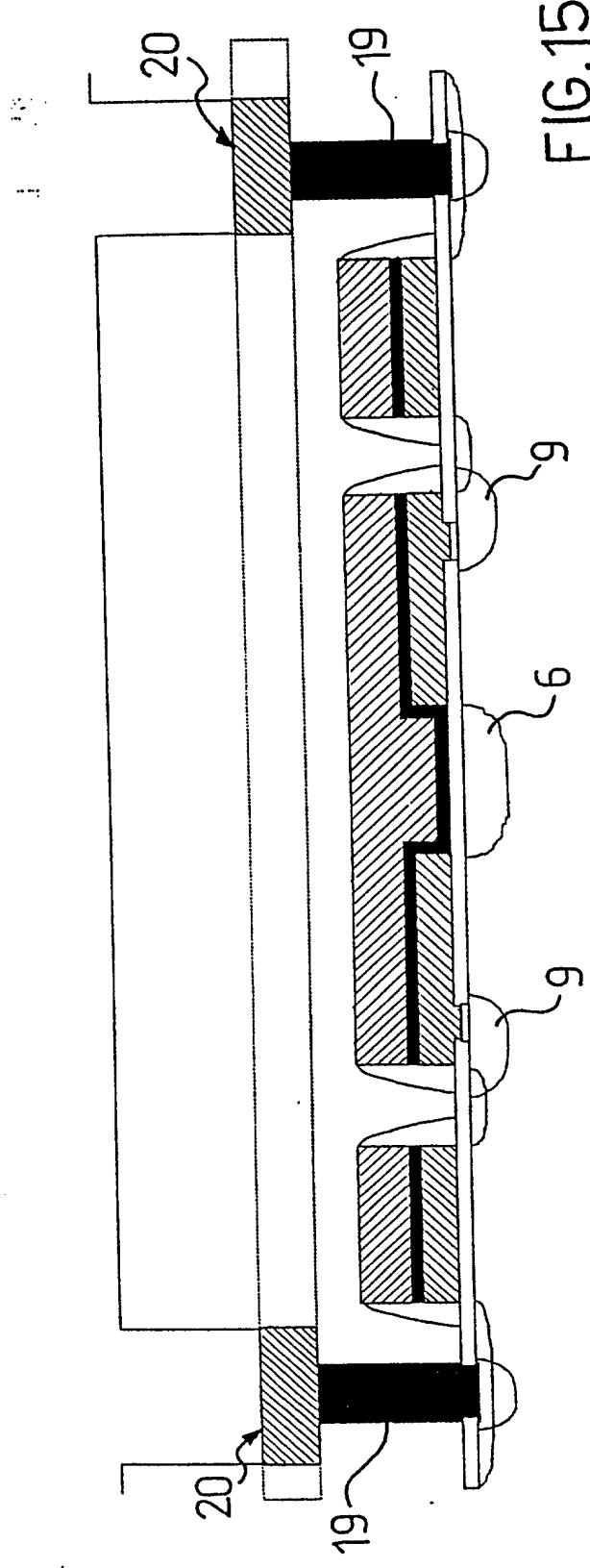


FIG. 15

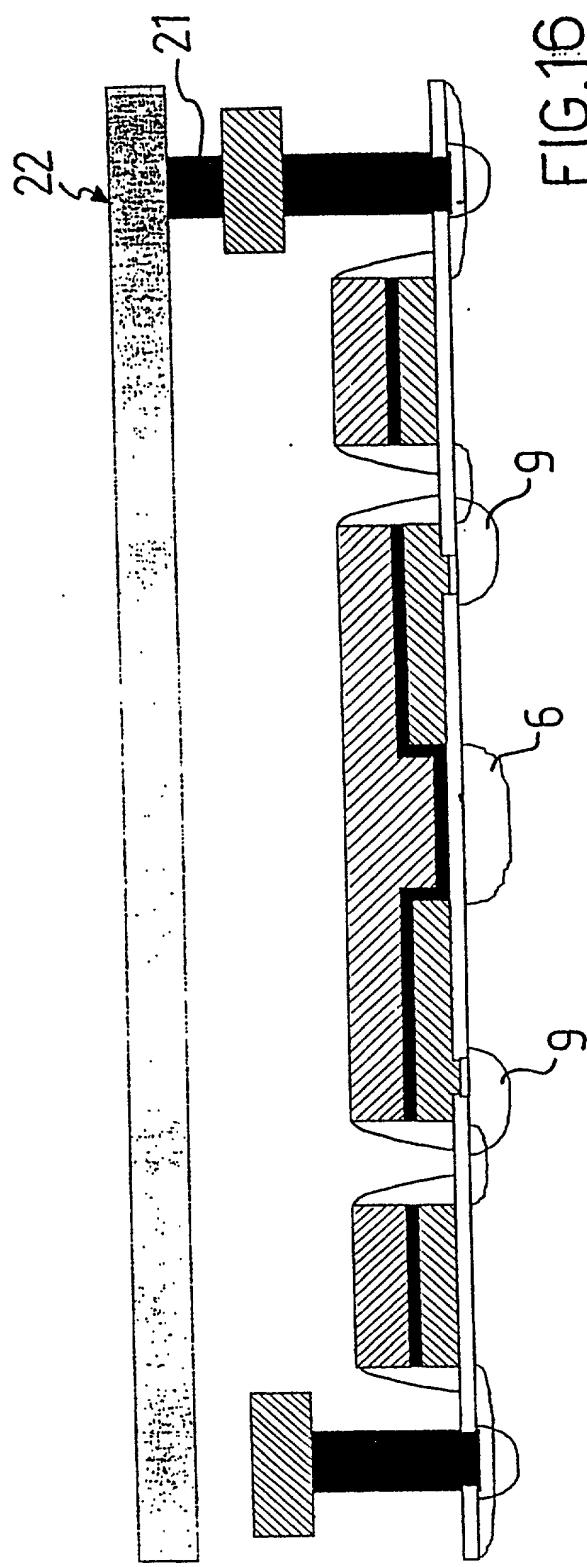


FIG. 16

FIG. 17. FIG. 18.

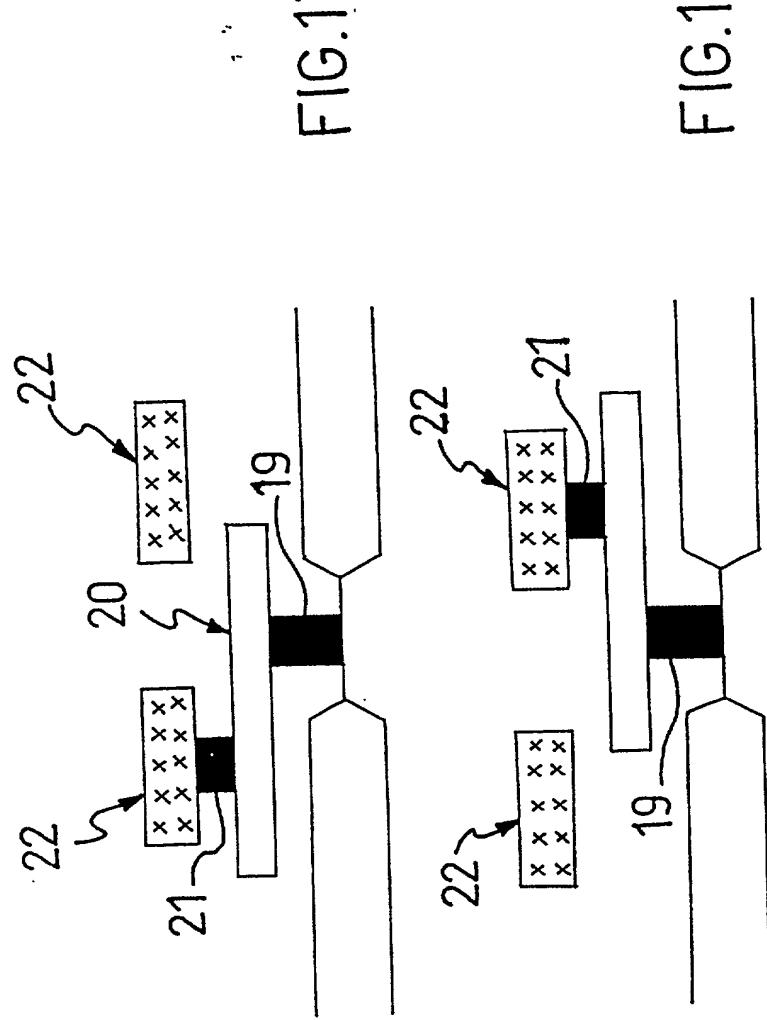


FIG.19

WL_m WL_{m+1} CG BL0 BL1...BL6 S BL7 S BL6..BL1 CG CG CG CG BL0 BL0 BL1

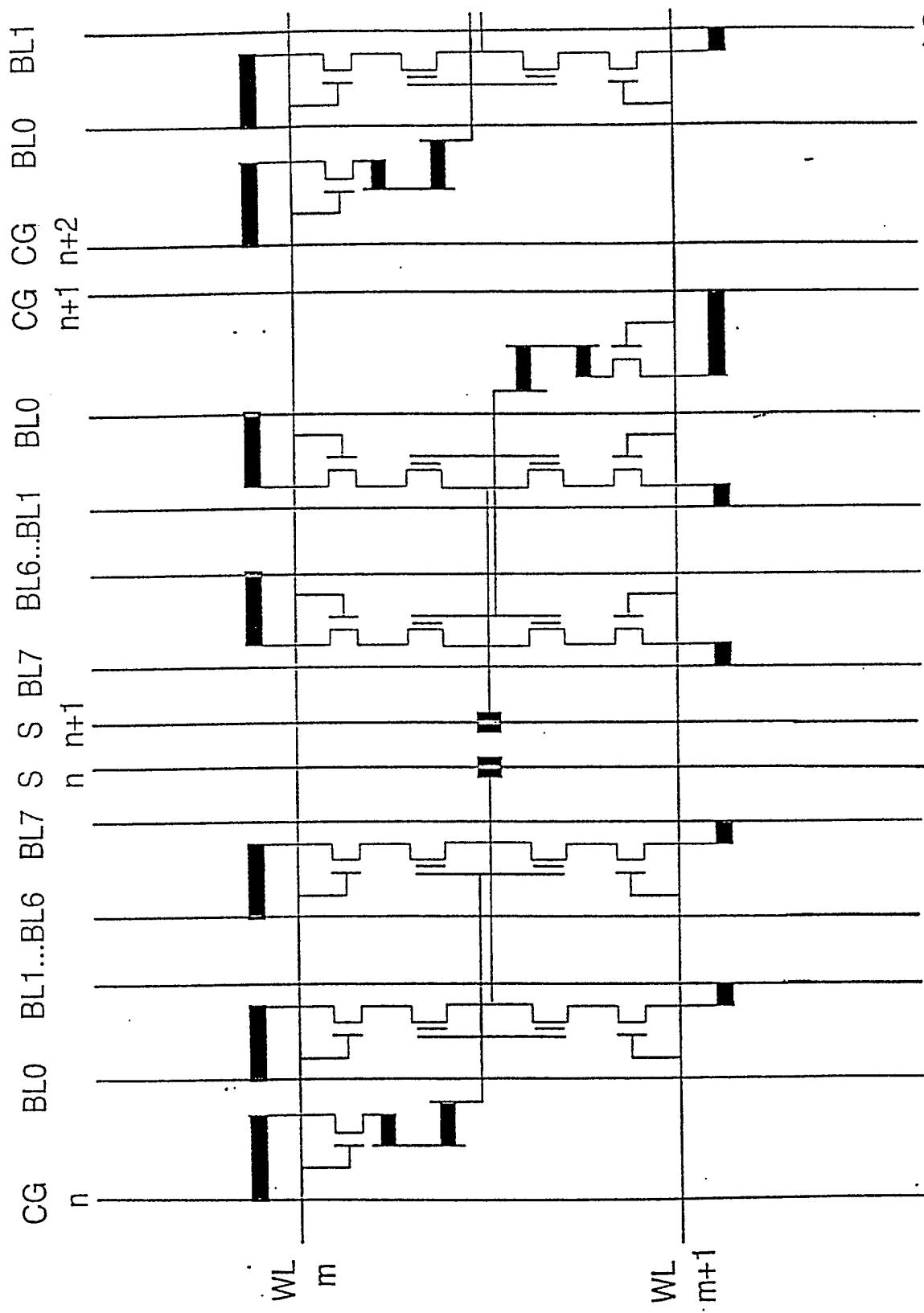


FIG.20

